

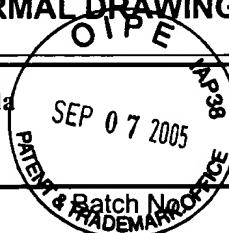
B/ISW

TRANSMITTAL OF FORMAL DRAWINGS

Docket No.

OKI.460

In Re Application Of: Yoshinori Shimosakoda



Serial No.

09/899,055

Filing Date

July 6, 2001

3171

Examiner

I. Moore

Art Unit

2661

Invention: SYNCHRONIZATION CORRECTION CIRCUIT

Address to:

Assistant Commissioner for Patents
Washington, D.C. 20231

Transmitted herewith are:

4 sheets of formal drawing(s) for this application.

Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c) on the reverse side of the drawing.

Signature

ANDREW J. TELESZ, JR.
REG. NO. 33,581

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Dated: September 7, 2005

I certify that this document and attached formal drawings are being deposited on September 7, 2005 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Signature of Person Mailing Correspondence

Typed or Printed Name of Person Mailing Correspondence